

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Use several sheets if necessary)Applicants
Harish G. PATIL et al.Filing Date
November 28, 2000Group
2183**REFERENCE DESIGNATION U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE

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Technology Center 2100

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	Translation YES NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AA	Combining Static and Dynamic Branch Prediction to Reduce Destructive Aliasing; Harish Patil et al. (12 p.).
AB	PhD Dissertation Static Methods in Branch Prediction; Donald Lindsay (2 p.).
AC	Intel® IA-64 Architecture Software Developer's Manual; Volume 1: IA-64 Application Architecture; Revision 1.1; July 2000 (4p.).

EXAMINER**DATE CONSIDERED**

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AA	Donald LINDSAY, <i>Static Methods In Branch Prediction</i> , Ph.D. Thesis submitted 1998 (147 p.).
AB	Intel® Itanium™ Architecture Software Developer's Manual, <i>Volume 1: Application Architecture</i> , Revision 2.0, December 2001 (234 p.).

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